

To fix this firmware bug you will need to.....

Cut the clock rate to the processor (pin 14) by a factor of 2 by including an extra flip flop into the 74HC393 divider chain. So cut the track to pin 9 of the 74HC393 and connect this track to pin 8 of the 74HC393. See the attached photo below.

Reload the Picaxe 28X2 with the new VK5JST code now up on the homepage. This doubles the gate time in the frequency counter function generating correct frequency displays. See the instructions elsewhere on the web page on how to load the Picaxe with code.

